

Docket No.: 0885-CS

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Joel Page et al.

Serial No. 09/153,864

Filed: September 16, 1998

For: POWER ON RESET TECHNIQUES FOR AN INTEGRATED CIRCUIT CHIP



Group Art Unit: 2816

Examiner: not assigned

5# / Per AA
2/2/99
J. Kim

INFORMATION DISCLOSURE STATEMENT

Honorable Commissioner of
Patents and Trademarks
Washington, D. C. 20231

RECEIVED

JAN 12 1999

Sir:

GROUP 2100

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of

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a first Office Action on the merits. No certification or fee is required.

Respectfully submitted,

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